

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) r 1.97(c))

Docket No.
DDM00-044

In Re Application Of: **Cahit Gezgin and Chris Morrow Young**

Serial No.

Filing Date
November 14, 2000

Examiner

Group Art Unit

Title: **METHOD AND APPARATUS FOR PREDICTING TRANSIENT RESPONSE
OF A CLOSED LOOP APPARATUS**

Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after three months of the filing of a national application, or the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or after the mailing date of a first Office Action on the merits, whichever occurred last but before the mailing date of either:

1. a Final Action under 37 CFR 1.113, or
 2. a Notice of Allowance under 37 CFR 1.311,
- whichever occurs first.

Also submitted herewith is:

- ☐ a certification as specified in 37 CFR 1.97(e);

OR

- ☐ the fee set forth in 37 CFR 1.17(p) for submission of an Information Disclosure Statement under 37 CFR 1.97(c).

JCE29 U.S. PRO
09/12/97

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Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. **12-2325** as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of _____
- ☒ Credit any overpayment.
- ☒ Charge any additional fee required.

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I certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. _____)

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Certificate of Mailing by First Class Mail

I certify that this document and fee is being deposited on Nov 14, 2000 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature of Person Mailing Correspondence

Donald D. Mondul

Typed or Printed Name of Person Mailing Correspondence

*This certificate may only be used if paying by deposit account.

Signature

Dated: November 14, 2000

Donald D. Mondul
Reg. No. 29,957
Attorney for Applicant

CC:

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

DDM00-044

Application Number

Applicant(s)

Cahit Gezgin and Chris Morrow Young

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	IF APPROPRIATE
	1	2,123,178	Jul 12, 1938	Hendrik W. Bode	179	171	NA

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	A	H.W. Bode, "Relations Between Attenuation and Phase in Feedback Amplifier Design"; Bell System Technical Journal, July 1940.
	B	V. Joseph Thottuvelil and George C. Verghese, "Analysis and Control Design of Parallelled DC/DC Converters With Current Sharing"; IEEE Transactions on Power Electronics; July, 1998.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INITIAL

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

C

H. Nyquist, "Regeneration Theory"; Bell System Technical Journal, January 1932.

D

Allen F. Rozman and Kevin J. Foellhoelter, Circuit Considerations for Fast, Sensitive, Low-Voltage Loads in a Distributed Power System", APEC 1995 Conference Proceedings, 1995.

E

Fundamentals of Power Electronics by Robert W. Erickson; Kluwer Academic Publishers, Boston, MA; 1997.

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